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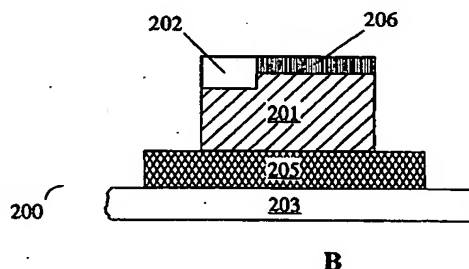
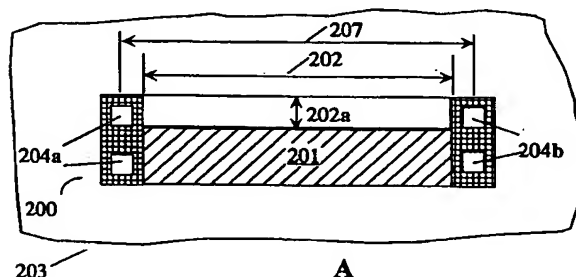
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[Continued on next page]

(54) Title: POLY-SILICON STRINGER FUSE



(57) Abstract: A polysilicon silicide stringer fuse is constructed having a narrow width by using an overlay tolerance of the photo stepper tool instead of the minimum critical dimension tolerance of the stepper tool. In an example embodiment, a fuse (200) for integration within a semiconductor comprises depositing an insulating layer (205) adjacent to the semiconductor substrate (203). A silicon layer (201) is formed with a first resistance deposited adjacent the insulating layer (205). The silicon layer has a first width. A metal silicide stringer (202), having a second resistance different from the first resistance is deposited over a portion of the first silicon material (201) and having a second width that is less than the first width within at least a portion thereof. The metal silicide conducts current therethrough with approximately the second resistance and agglomerates in response to a programming current other than the conduct current therethrough with a same second resistance.

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Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC, IBM-TDB

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Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Y	US 4 984 054 A (YAMADA MICHIMIRO ET AL) 8 January 1991 (1991-01-08) abstract; claims; figures 5-13 column 5, line 30 - line 47 column 6, line 40 - line 47 ---	1,2,5-8
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☒ Further documents are listed in the continuation of box C.

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